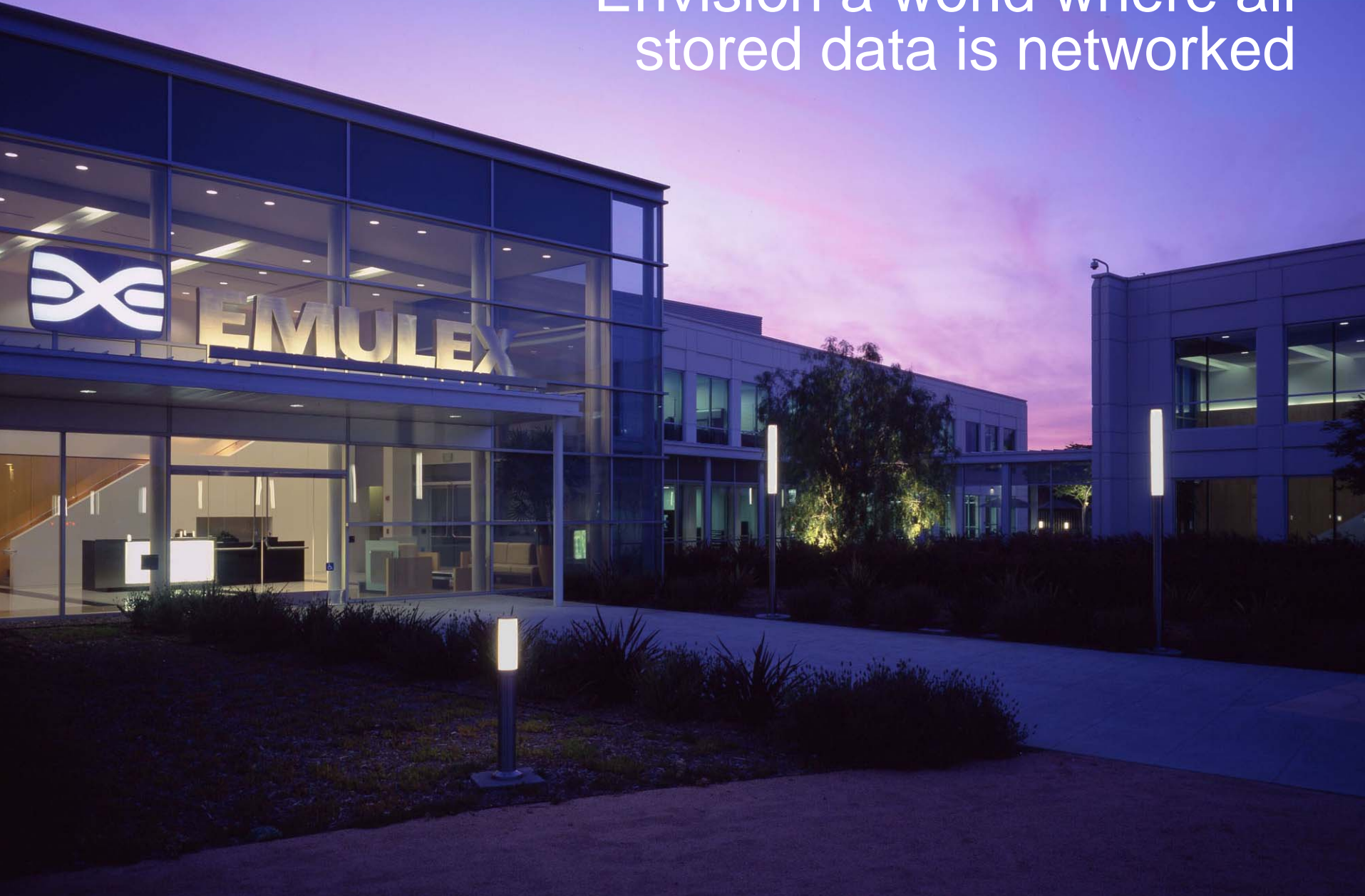


Envision a world where all
stored data is networked



SystemC: A User's Perspective

What's Working and What Needs to be Improved, at NASCUG VI

Terry Doherty
Principal Engineer
Emulex Corporation

SystemC at Emulex

- Architecture Exploration
 - Performance goals, interoperability issues.
- Design Refinement
 - Bus sizing, buffer sizing.
- System Analysis
 - Evaluate very large systems and specific customer configurations.
- Virtual Prototyping
 - Needs to be fast enough for day to day firmware debugging.
- SystemC
 - Portable
 - Extendable

SystemC Enhancements

- Separate PV from model dataflow
 - Allows different data flow models for different purposes
 - Performance analysis vs. firmware debugging
 - Registers and Memory
- Seamless integration of SystemVerilog and SystemC
 - Standardized SystemC/SystemVerilog API
- Object Oriented ASIC Design

Jack's Assigned Mystery Topic

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